Librarian Learning Resource Centre Acusina institute & Technology

Eighth Semester B.E. Degree Examination, Feb./Mar. 2022 **Digital Switching Systems**

Max. Marks:100 Time: 3 hrs.

Time: 3 nrs.			
Note: Answer any FIVE full questions, selecting at least TWO full questions from each part.			
PART - A			
1	a. b. c.	With a neat diagram, explain the hierarchy used in the national Public Switched Network (PSTN). Explain the 3D-channel PCM primary multiplex, with a neat diagram. A four wire circuit has an overall loss (Two-wire to two-wire) of 1db and the bala loss at each end is 6db. Find: i) The singing point ii) The stability	(08 Marks) (08 Marks) nce return
2	a. b.	Differentiate between circuit switching and message switching. With a neat block diagram, explain subcriber's line interface circuit for a digital sw Explain the basic functions of a switching systems.	(06 Marks) vitch. (06 Marks) (08 Marks)
	0.		D 1
3	a.	Define the following terms. I) Trustile internet	Busy hour (04 Marks)
	b.	iv) Holding time. Derive an expression for the second Erlang's distribution starting from basic princ	
	υ.		(12 Mains)
	C.	On an average, one call arrives every 5 seconds. During a period of 10 seconds. The probability that i) No call arrives ii) One call arrives iii) Two call arrives	
		iv) More than two calls arrive.	(04 Marks)
1	0	What is Grading? Explain types of grading.	(08 Marks)
4	a. b.	Explain two stage switching network with necessary equations.	(08 Marks)
	c.	- to a local for 100 incoming trunks and 400 outgoing Ituliks.	(04 Marks)
5	a.	PART – B Explain the principle operation of T-S-T network and S-T-S network. With neat of Explain frame alignment of PCM signals in digital exchange.	diagram. (12 Marks) (08 Marks)
	U.		(10 Mayles)
6	a b	1	(10 Marks) (10 Marks)
7	' a	Write a short notes on the interfaces of a typical digital switching system central	office. (10 Marks)
		For the methodology used for reporting and correcting of field problems.	(10 Marks)

Explain the methodology used for reporting and correcting of field problems. (10 Marks)

Write a short note on digital switching system hardware architecture. (10 Marks) 8

Explain the three level scheme of recovery strategy in a digital switch. (10 Marks)