## Fifth Semester B.E. Degree Examination, Dec.2023/Jan.2024 Microcontroller

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, selecting at least TWO full questions from each part.

| Note: Answer any FIVE Juli questions, selecting at least 1770 July |   |  |               |  |
|--|---|--|---------------|--|
|  |   | PART - A   |               |  |
|  |   |  | (10 Marks)    |  |
| 1  | a.  | 1 1: Cf-marage hetyroon micronrocessor and a lillelocollulous.   |               |  |
|  | b.  | Bring out the architectural differences between interopression   | (04 Marks)    |  |
|  |   | Explain the working principle of input & output operation of Port $-1$ .   | (06 Marks)    |  |
|  | C.  |  |               |  |
|  |   | a. Explain the different addressing modes of 8051 with examples and mention their limitations.  (10 Marks)   |               |  |
| 2  | a.  | Explain the different addressing modes of 6031 with sharper  | (10 Marks)    |  |
|  | 4   | Explain the following instructions with suitable examples:   |               |  |
|  | b.  |  |               |  |
|  |   | I) SWAI A  | (10 Marks)    |  |
|  |   | iv) XCHD A, @Ri v) DAA   |               |  |
| 2  | 0   | Briefly explain the various types jump instruction.  | (06 Marks)    |  |
| 3  | a.  | Diagrage the subroutine instriction will execution steps.  | (08 Marks)    |  |
|  | b.  | What is subroutine? Discuss the subroutine instruction was all the write an ALP to generate Fibonacci series upto given value 'n' and store all the write an ALP to generate Fibonacci series upto given value 'n' and store all the write and t | he n bytes    |  |
|  | C.  | starting from memory location 30h.   | (06 Marks)    |  |
|  |   | starting from memory location 5 val  |               |  |
|  |   | Discuss the data types in 8051C.   | (06 Marks)    |  |
| 4  | a.  | 2074 C 1- to colo all the bits of Pl and P/ Continuously with 25   | ) ms delay.   |  |
|  | b.  |  |               |  |
|  |   | 2051 C to convert a hex-data UFFN III to its equivalent decima   | al data and   |  |
|  | C.  | Write a 8051C program to convert a non-access and program to convert a no-access and program to convert a no-access and program to convert | (08 Marks)    |  |
|  |   | display the result digits on P0, P1, P2.   |               |  |
| PART - B   |   |  |               |  |
|  |   | Explain different modes of operation of timer/counter with relevant block diagran  | 1.            |  |
| 5  | a.  | Explain different modes of operation of times country.   |               |  |
|  | 1   | Explain the bit pattern of TMOD and TCON SFR register.   | (08 Marks)    |  |
|  | b   | Explain the oil pattern of Thios and   |               |  |
|  |   | Explain SCON register with its bit pattern.  | (06 Marks)    |  |
| 6  |   | 1 to 11 11 11 11 11 11 11 11 11 11 11 11 11  | (06 Marks)    |  |
|  | b   | Explain the different steps in recurring data sortary assignments. Write a program to send the data message "MICROCONTROLLERS"   | of length     |  |
|  | C   | 17 character at a band rate Q400, 8 bit data, 1 stop bit serially.   | (08 Marks)    |  |
|  |   |  |               |  |
| _  | _   | What is interrupt? List out the difference between interrupt and polling method.   | (06 Marks)    |  |
| 7  |   | finterprints and also write the settleflet for interrupt ones  | ution.        |  |
|  | b   | Explain various types of interrupts and also write the sequence  |               |  |
|  |   | e. Explain bit pattern of IE and IP.   | (06 Marks)    |  |
|  | (   |  |               |  |
|  | 8 a. Show the interfacing circuit and functional pins of LCD. Write a C program to disp |  |               |  |
|  | 8 8   |  |               |  |
|  |   | of the interfering of stepher motor to 8051. Write 8051 assembly of C progr  | ram to rotate |  |
|  | -   | b. Show the interfacing of stepper motor to deep stepper motor in anticlockwise rotation with appropriate delay.   | (10 Marks)    |  |
|  |   | stepper motor manustockwise returns.   |               |  |

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